ARTIS

SILICON SPRINT



Explore the full chip design journey to build hands-on skills and learn using industry-standard Cadence tools.

8 WEEKS
Duration

BDT 15,000
COURSE FEE
Scholarship opportunity for outstanding students

CLASS SCHEDULE
3 Days/Week
3 Hours/Day

LOCATION: Aristo Tower (Level 3), 239 Bir Uttam Mir Shawkat Sarak Tejgaon-Gulshan Link Road, Dhaka 1208

Course Outcome

- A tape-out-ready GDSII block
- A documented repository for hiring managers

Who Can Apply?

Final-year students or recent graduates from:
EEE | ECE | ETE | CSE | Applied Physics/Electronics & related disciplines

Screening Process

Written Assessment

COURSE CURRICULUM

Week 0	Bridge
Week 1	Custom Design
Week 2	Analog Design
Week 3	RTL Design, Verification & Synthesis
Week 4	Physical Design
Week 5	Physical Verification
Week 6	Timing & Power Signoff
Week 7	Capstone - Front-End Closure
Week 8	Capstone - Back-End & Defence

Apply Now

www.acisemi.com

For upcoming course information
artis@aci-bd.com